Addressing, Link and Network

Test Number	Test Subsection	Description	
C4-001	C04-001 & 02	Default GUID Check	Core
C7-001	C07-01-01	Normal Link Init to Active Path	Core
	C07-01-02	Link Active State Test	Core
	C07-01-03	Link ActiveDefer State Test	Core
	C07-01-04	Link Arm State Test	Core
	C07-01-05	Link Initialize State Test	Core
C7-007	C07-07-01	Packet with Bad VCRC	Core
	C07-07-02	Packet with Bad ICRC	Core
	C07-07-03	Packet with bad LVer	Core
	C07-07-04	Packet with Bad PktLen	Core
	C07-07-05	Packet with bad DLID	Core
	C07-07-06	Packet with Bad VL	Core
	C07-07-07	Packet with GRH and VL15	Core
	C07-07-08	Buffer Overflow	Core
	C07-07-09	Bad Pkt	Core
	C07-07-10	Marked Bad Packet	Core
C7-029	C07-29-01	MaxVL Check	Core
	C07-29-02	Less than Max VL Check	Core
	C07-29-03	Illegal Operational VL	Core
C7-030	C07-030-01	Link Initialize Send Link Packets Check	Core
	C07-030-02	Link Arm Send Link Packets Check	Core
	C07-030-03	Link Active Send Link Packets Check	Core
	C07-030-04	Link Initialize Receive Link Packets Check	Core
	C07-030-05	Link Arm Receive Link Packets Check	Core
	C07-030-06	Link Active Receive Link Packets Check	Core
C7-031	C07-031	Minimum Buffer Resources per VL	Core
C7-032	C07-032	Data Packets Shall Be Placed In The Associated Vl	Core
C7-033	C07-033	SL on Packets must be Invariant in a Subnet	2 Gen
C7-040	C07-040-01	Verifies LNH field for Local Packets	Core
	C07-040-02	Verifies LNH field for Global Packets	Core
C7-046	C07-046	SLID Assignment Check	Core
C7-053	C07-53-01	Flow Control - Op Link Initialize	Core
	C07-53-02	Flow Control - Op Link Arm	Core
	C07-53-03	Flow Control - Op Link Active	Core
C7-058	C07-058	Flow Control - FCTBS - Initialization	Core
C7-062	C07-062	Flow Control - Verify values as FCTBS accumulates	Core
C7-066	C07-066-01	LID Mask Control (LMC) Check	Core
	C07-066-02	Sub LMC Check	Core
C7-069	C07-069-01	Error Detection - Initialize	Core
	C07-069-02	Error Detection - Armed	Core
	C07-069-03	Error Detection - Active	Core
C7-070	C07-070#02	Control Update Watch Dog - ARM	Core
	C07-070#03	Control Update Watch Dog - Active	Core
C8-006	C08-006	GRH Rule for PayLen Value	Core
C8-010	C08-010 & 011	GRH Rule for SGID Value	Core

Management

Test Number	Test Subsection	Description	
C13-013	C13-013	Response Time Values	**Core**
C13-014#01	C13-014#01	Response Time Value Single Packet	**Core**
C13-014#02	C13-014#02	RespTimeValue Non Subn & Non Dir Route	**Core**
C13-026	C13-026	Attribute Modifier not used	**Core**
C13-045.1.1 MAD Validation	C13-045.1.1	MAD Validation - Part 1	**Core**
	C13-045.1.1	MAD Validation - Part 2	**Core**
	C13-045.1.1	MAD Validation - Part 3	**Core**
	C13-045.1.1	MAD Validation - Part 4	**Core**
	C13-045.1.1	MAD Validation - Part 5	**Core**
	C13-045.1.1	MAD Validation - Part 6	**Core**
	C13-045.1.1	MAD Validation - Part 7	**Core**
	C13-045.1.1	MAD Validation - Part 8	**Core**
	C13-045.1.1	MAD Validation - Part 9	**Core**

Subnet Management

Test Number	Test Subsection	Description	
C14-006	C14-006	Basic Directed Route SMP Request	Core
C14-009#01	C14-009#01	Basic Directed Route SMP Through a Switch	2 Gen
C14-009#02	C14-009#02	Basic DR SMP Through a Switch with DrDLID Known	2 Gen
C14-009#03	C14-009#03	Basic DR SMP Through a Switch with DrSLID Known	2 Gen
C14-009#04 Directed Route	C14-009#04-01	Error handling of DR SMP by xCA - HP < HC	Core
	C14-009#04-02	Error handling of DR SMP by xCA - HP=HC DrDLID != 0xFFFF	Core
	C14-009#04-03	Error handling of DR SMP by xCA - HP >= HC + 2	Core
C14-010	C14-010	Directed Route SMP Field Initialization	Core
C14-011	C14-011	Directed Route SMP with DrDLID known	Core
C14-013 Directed Route	C14-013-01	SMP Request with Invalid Port # in the Initial Path	2 Gen
	C14-013-02	SMP Request with Hop Ptr = Hop Count + 2	2 Gen
	C14-013-03	SMP Response - invalid port number in return path	2 Gen
	C14-013-04	SMP Response with Hop Ptr = Hop Count + 2	2 Gen
C14-015	C14-015	No M_Key Checking	Core
C14-016_Get	C14-016_Get	M_Key Checking for SubnGet	Core
C14-016_Set	C14-016_Set	M_Key Checking for SubnSet	Core
C14-016_Trap	C14-016_Trap	M_Key Checking for SubnTrapRepress	Core
C14-017	C14-017	M_key Lease Period Timer	Core
C14-018	C14-018	M_Key Violation counter	Core
C14-023	C14-023	M_KEY Components in NVRAM	Core
C14-024#02	C14-024#02	NodeDescription	Core
C14-024#03	C14-024#03	NodeInfo	Core
C14-024#04 SwitchInfo	C14-024#04-01	SwitchInfo - Read Only Components	Core
	C14-024#04-02	SwitchInfo - Read Write Components Part 1	Core
	C14-024#04-03	SwitchInfo - Read Write Components Part 2	Core
	C14-024#04-04	SwitchInfo - Down-to-Initialize and Initialize-to-Down Transitions	2 Gen
	C14-024#04-05	SwitchInfo - Armed-to-Down Transition	2 Gen
	C14-024#04-06	SwitchInfo - Active-to-Down Transition	2 Gen
C14-024#05	C14-024#05	GUIDInfo	Core
C14-024#06 PortInfo xCA	C14-024#06-01	PortInfo for xCA and Router only - Part 1	Core
	C14-024#06-02	PortInfo for xCA and Router only - Part 2	Core
	C14-024#06-03	PortInfo for xCA and Router only - Part 3	Core
	C14-024#06-04	PortInfo for xCA and Router only - Part 4	Core
	C14-024#06-05	PortInfo for xCA and Router only - Part 5	Core
	C14-024#06-06	PortInfo for xCA and Router only - Part 6	Core
C14-024#06 PortInfo Switch	C14-024#06-01	PortInfo for Switch only - Part 1	Core
	C14-024#06-02	PortInfo for Switch only - Part 2	Core
	C14-024#06-03	PortInfo for Switch only - Part 3	Core
	C14-024#06-04	PortInfo for Switch only - Part 4	Core
	C14-024#06-05	PortInfo for Switch only - Part 5	Core
	C14-024#06-06	PortInfo for Switch only - Part 6	Core
	C14-024#06-07	PortInfo for Switch only - Part 7	Core
C14-024.1.1#06	C14-024.1.1#06	PortInfo LocalPortNum	Core
C14-024#07 P_Key	C14-024#07-01	Partition Key Table for xCA and SW Mgmt Port 0	Core
	C14-024#07-02	Partition Key Table for Switch Only - UnSupported Attribute	Core
	C14-024#07-03	Partition Key Table for Switch Only - Supported Attribute - Part 1	Core
	C14-024#07-04	Partition Key Table for Switch Only - Supported Attribute - Part 2	Core
	C14-024#07-05	Partition Key Table for Switch Only - Supported Attribute - Part 3	Core
C14-024#08 SL2VL xCA	C14-024#08-01	SLtoVLMapping Table - xCA - Supported	Core
	C14-024#08-02	SLtoVLMapping Table - xCA - UnSupported	Core
C14-024#08 SL2VL Switch	C14-024#08-03	SLToVLMapping Table for Switches Only	Core
	C14-024#08-04	SLtoVLMapping Table - Switch - UnSupported	Core
	C14-024#08-05	SLtoVLMapping Table - Switch - Supported	Core
C14-024#09 VLArb	C14-024#09	VLArbitrationTable - xCA and Routers	Core
	C14-024#09	VLArbitrationTable - Switch	Core
C14-024#10 LFT	C14-024#10-01	LinearForwardingTable - Supported - Invalid Ports	Core
	C14-024#10-02	LinearForwardingTable - Supported - Valid Ports	Core
	C14-024#10-03	LinearForwardingTable - UnSupported	Core
C14-024#11	C14-024#11	RandomForwardingTable	Core
C14-024#12	C14-024#12	MulticastForwardingTable	Core
C14-024#13 SMInfo	C14-024#13-01	SMInfo - Supported - Part 1	Core
	C14-024#13-03	SMInfo - UnSupported	Core
C14-024#15	C14-024#15	LedInfo	Core
C14-024#LinkWidthNeg	C14-024#LinkWdithNeg	PortInfo Link Width Negotiation	Core
C14-041.1.1	C14-041.1.1	SM Yield Part 1	Core
C14-041.1.1	C14-041.1.1	SM Yield Part 2	Core
C14-041.1.1	C14-041.1.1	SM Yield Part 3	Core
C14-041.1.1	C14-041.1.1	SM Yield Part 4	Core

Subnet Administration

Test Number	Test Subsection	Description	
C15-0.1.012#15	C15-0.1.012#15	SubnAdmGet(ServiceRecord)	**Core**
C15-0.1.012#17.01	v1c15-0.1.012#17.01	SubnAdmGet(PathRecord)	**Core**
C15-0.1.012#17.02	C15-0.1.012#17.02-Part1	SubnAdmGetTable(PathRecord) Part 1	**Core**
	C15-0.1.012#17.02-Part2	SubnAdmGetTable(PathRecord) Part 2	**Core**
	C15-0.1.012#17.02-Part3	SubnAdmGetTable(PathRecord) Part 3	**Core**
SM-SA Validation	SM-SA Validation	SM-SA Validation Testing	**Core**